

The diagram illustrates the architecture of an electronic camera system (6) and its connection to a PHS unit (7).

Electronic Camera (6):

- Optical Path:** Light enters through a lens (31) and is captured by a CCD sensor (32).
- Signal Processing:** The CCD output is converted by an A/D converter (33) and stored in a buffer memory (34).
- Image Processing:** Data from the buffer memory is sent to the image processing section (35), which handles compression and expansion.
- Storage:** The processed image data is managed by a memory control circuit (36) and stored on a memory card (37).
- User Interface & Control:** A key input section (40) sends commands to the control section (38). The control section (38) manages the camera's operations, including the display section (39) which serves as an electronic viewfinder (EVF). A guide panel (12) is also connected to the control section.

PHS Unit (Subsidiary Unit) (7):

- Power & Control:** A power switch (22) is connected to the control section (52).
- Communication:** The control section (52) is linked to a communication/control section (54) via a data buffer (53). The communication section (54) is connected to an antenna (55) for wireless transmission.

Inter-unit Connection: The electronic camera (6) and the PHS unit (7) are connected via two interface (I/F) sections, labeled 41 and 51, which facilitate data exchange between the two systems.

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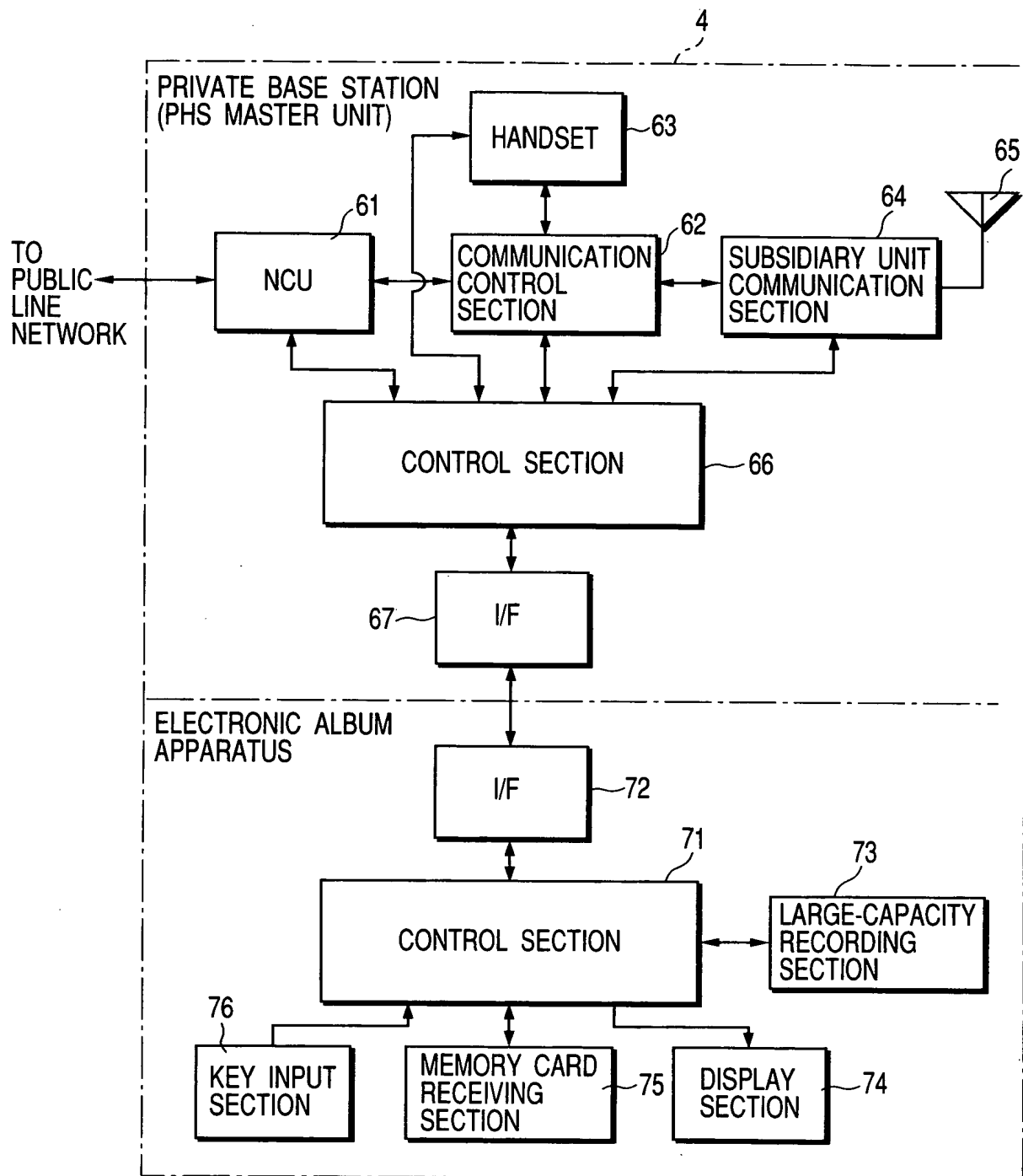


FIG. 4

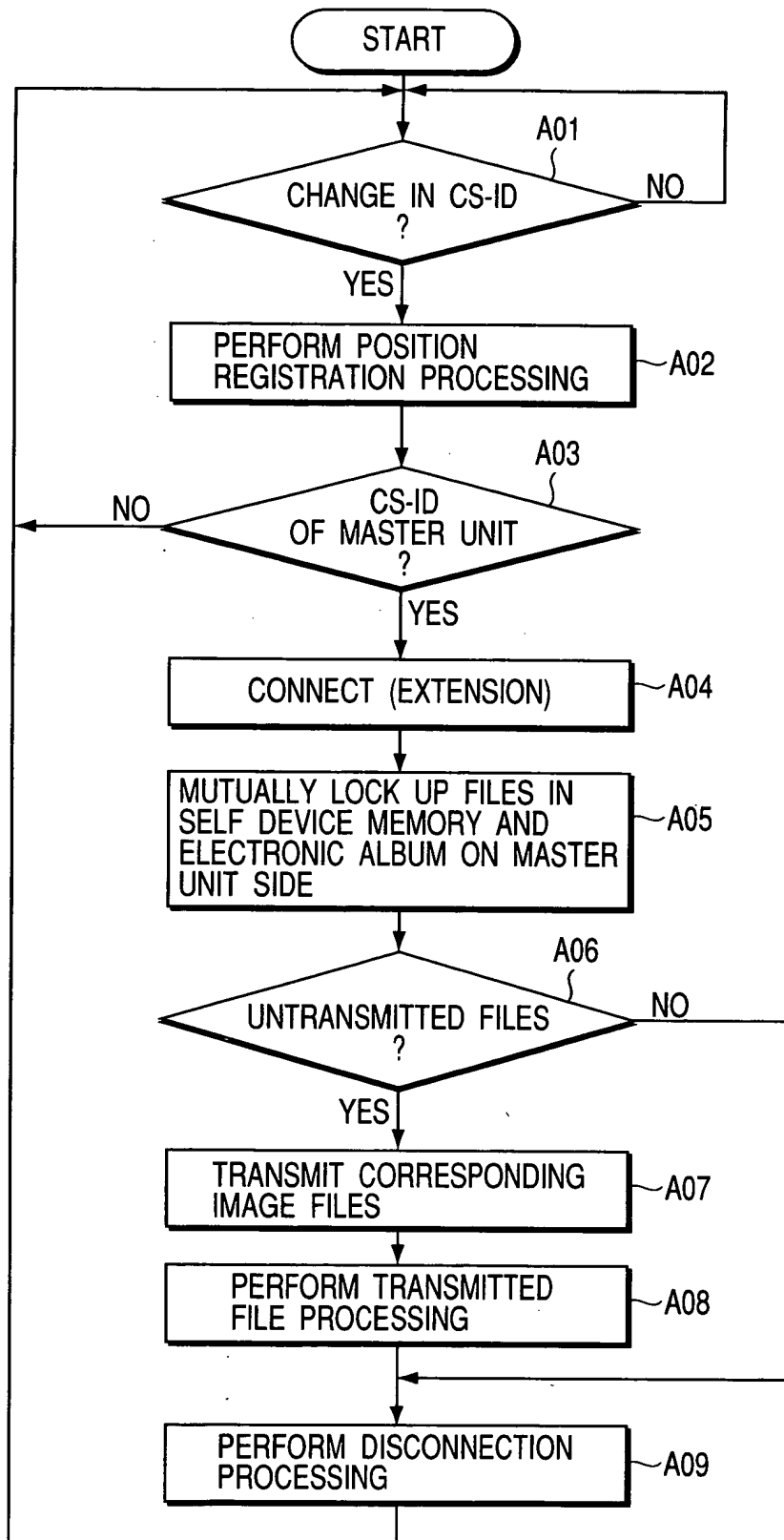


FIG. 5

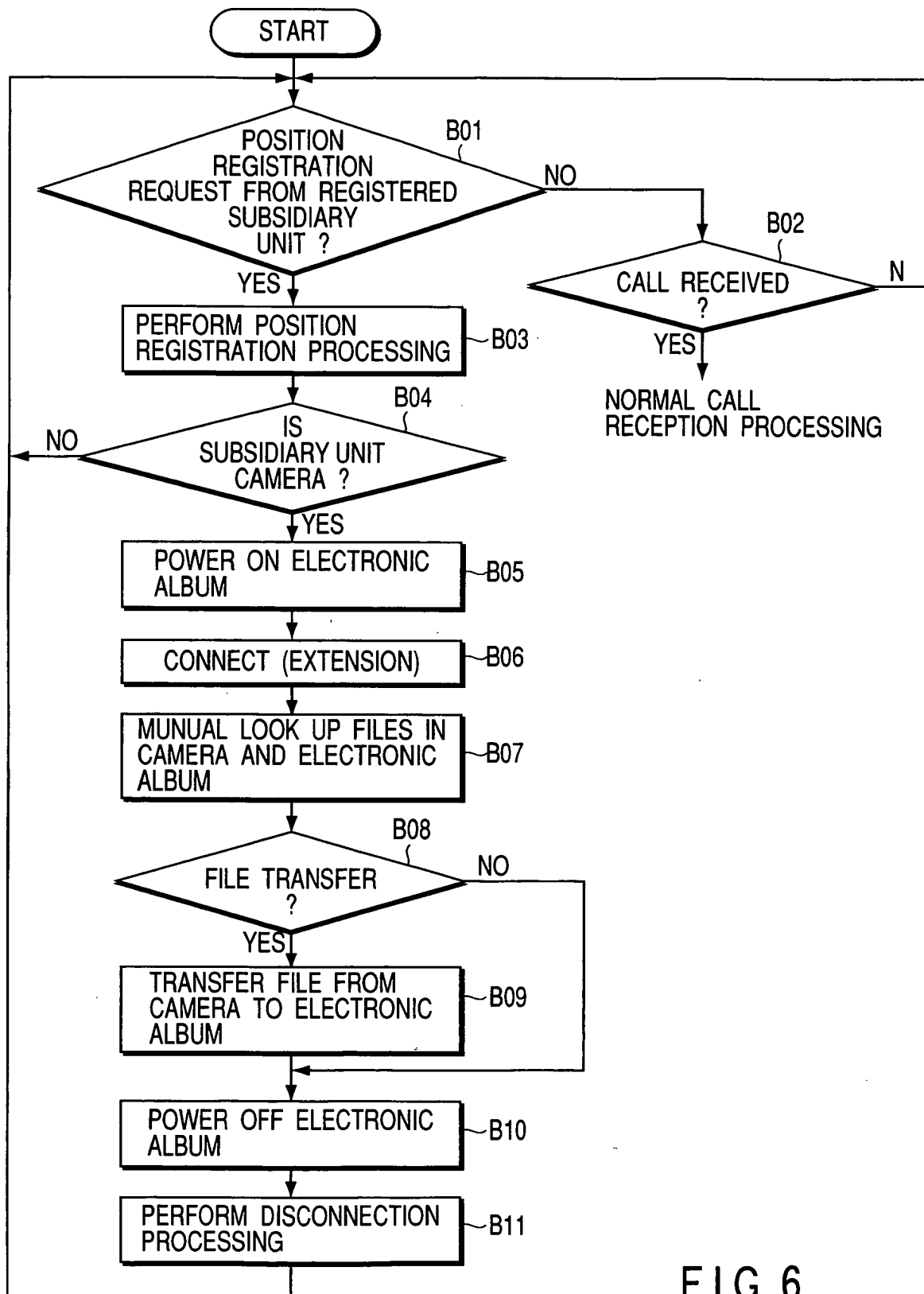


FIG. 6